



Docket No: 02986.P042

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Naresh Maheshwari, et al.

Application No.: 10/646,657

Filing Date: August 21, 2003

For: METHOD AND APPARATUS FOR  
CIRCUIT DESIGN AND SYNTHESIS

)  
) Examiner: Tat, Binh C.

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) Art Unit: 2825

)  
) Confirmation No. 9938

)  
) Date of NOA: 06/22/2006

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

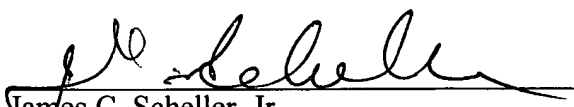
Dear Sir:

Applicant is assuming that the Examiner's statement of reasons for allowance is to be taken in light of the structure and interaction recited in the claims. Applicant notes that the Examiner's comments may have paraphrased the language of the claims and it should be understood that the language of the claims themselves set out the scope of the claims.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: 9/20, 2006

  
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450

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